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APPLICANTS

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**** CONTINUING DATA *******

This application is a CON of 09/381,377 09/20/1999 PAT 6,708,301 which is a 371 of PCT/JP98/01249 03/20/1998

**** FOREIGN APPLICATIONS *******

JAPAN 9-067667 03/21/1997

IF REQUIRED, FOREIGN FILING LICENSE GRANTED **

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| Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no | STATE OR COUNTRY JAPAN | SHEETS DRAWING 22 | TOTAL CLAIMS 21 | INDEPENDENT CLAIMS 3 |
| 35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance | | | | |
| Verified and Acknowledged <u> </u> Examiner's Signature Initials | | | | |

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TITLE

Functional block for integrated circuit, semiconductor integrated circuit, method for testing semiconductor integrated circuit, and method for designing semiconductor integrated circuit

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